ABSTRACT

A symmetric differential domino carry generate gate. In an embodiment, the load for the true inputs is equal to the load for the compliment inputs. In another embodiment, the output drive strength for the true output is the same as the output drive strength for the compliment output. In another embodiment, the symmetric differential domino carry generate gate has a first evaluation block of transistors and a second evaluation block of transistors, and the second evaluation block has the same number of transistors connected in a parallel relationship as the first evaluation block and the same number of transistors connected in a serial relationship as the first evaluation block.